



Application No.: 09/245,269

Docket No.: INTECH 3.0-038  
99P7451US

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on the date shown below.

Dated: October 14, 2002

Signature: *Lawrence E. Russ*

(Lawrence E. Russ)

RECEIVED  
OCT 22 2002

REPLY UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
TECHNOLOGY CENTER 2800

Docket No.: INTECH 3.0-038  
99P7451US  
(PATENT)

*NE*  
*#*  
*13/B*  
*10-24-02*  
*J. Antea*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
J. Mandelman et al.

Application No.: 09/245,269

Group Art Unit: 2814

Filed: February 5, 1999

Examiner: D. Nguyen

For: FIELD-SHIELD-TRENCH ISOLATION FOR  
GIGABIT DRAMS

Box AF  
Commissioner for Patents  
Washington, DC 20231

AMENDMENT UNDER 37 CFR 1.116

Dear Sir:

In response to the Final Office Action dated July 15, 2001, Applicants submit the following amendments and remarks:

IN THE CLAIMS:

Cancel claims 3-20.

REMARKS

Reconsideration and allowance of this application is respectfully requested. Claims 3-20 are withdrawn. Claims 1-2 remain in this application and are submitted for the Examiner's consideration.

In the Office Action, the Examiner rejected claims 1-2 under 35 U.S.C. §112, first paragraph. Specifically, the Examiner asserts: "In claim 1, the phrase 'an isolation trench enclosing an area of the semiconductor body' is not understood and not described in the